Leaded Chip Terminations

Style LT1

General Specifications
- Nominal Impedance: 50 Ω
- Resistive Tolerance: ±5% standard, ±2% available
- Operating Temp Range: -55 to +150˚C
- Temperature Coefficient: ±150 ppm/˚C
- Resistive Elements: Tantalum, Thin Film Processed
- Substrate Material: Aluminum Nitride
- Terminals: Silver over Nickel
- Lead-Free, RoHS Compliant
- Reliability: MIL-PRF-55342

ATC Part Number Code

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<tr>
<th>Code</th>
<th>Case Style</th>
<th>Case Size</th>
<th>Tolerance</th>
<th>Resistant Tolerances</th>
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Power Derating

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